

CLAIMS:

1. A data carrier (DC)

configured to communicate with a communication station with the aid of a carrier signal (CS) having a given carrier signal frequency (f_c) and which includes a receiving-means configuration (RC) for receiving the carrier signal (CS),

5 which receiving-means configuration (RC) has the following elements, namely a switching means (S), which is switchable between a conductive switching state and a non-conductive switching state, and

a first transmission coil (L1) that can be short-circuited with the aid of the switching means (S), through which first transmission coil a coil current (I1) flows during a communication process with a communication station, during which process the switching means (S) is in its conductive switching state, which coil current (I1) has a phase lag with respect to the voltage (U1) across the first transmission coil L1, and

a capacitor configuration (CC) arranged in parallel with at least one second transmission coil (L2), the presence of said capacitor configuration causing a coil current (I2) through this transmission coil (L2) during a communication process with a communication station, during which process the switching means (S) is in its conductive switching state, which coil current (I2) has a phase lead with respect to the voltage (U2) across the second transmission coil (L2),

characterized in that

20 the receiving means configuration (RC) is configured to be controllable as regards the value of at least one of its elements comprising the at least one second transmission coil (L2) and the capacitor configuration (CC).

2. A data carrier (DC) as claimed in claim 1, characterized in that

25 of the at least one second transmission coil (L2) and the capacitor configuration (CC) only the capacitor configuration (CC) is configured to be controllable as regards its capacitance value.

3. A data carrier (DC) as claimed in claim 2, characterized in that

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4. A data carrier (DC) as claimed in claim 3, characterized in that the capacitor configuration (CC) includes a capacitor (C) and at least one series arrangement of a further capacitor (C1) and a further switching means (S1) arranged in parallel with the capacitor (C), and the further switching means (S1) is switchable between a conductive switching state and a non-conductive switching state.

5. A data carrier (DC) as claimed in claim 1, characterized in that the capacitor configuration (CC) is only arranged in parallel with the at least one further transmission coil (L2) and is consequently disposed in series with the first transmission coil (L1).

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